

FIG. 1

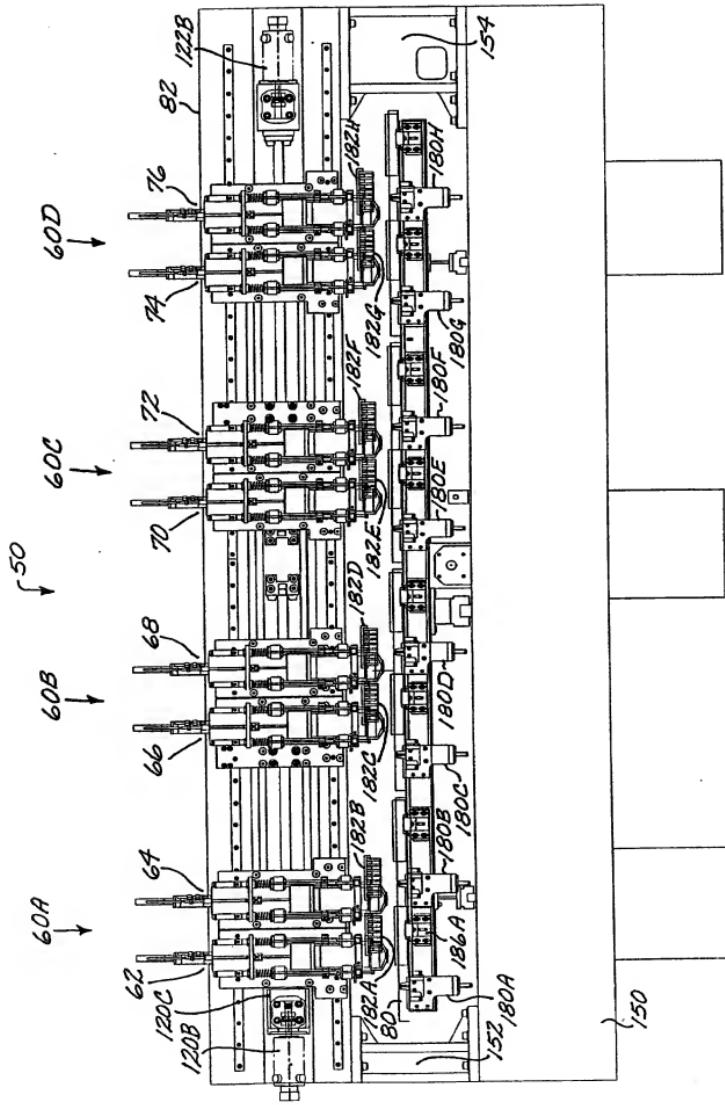
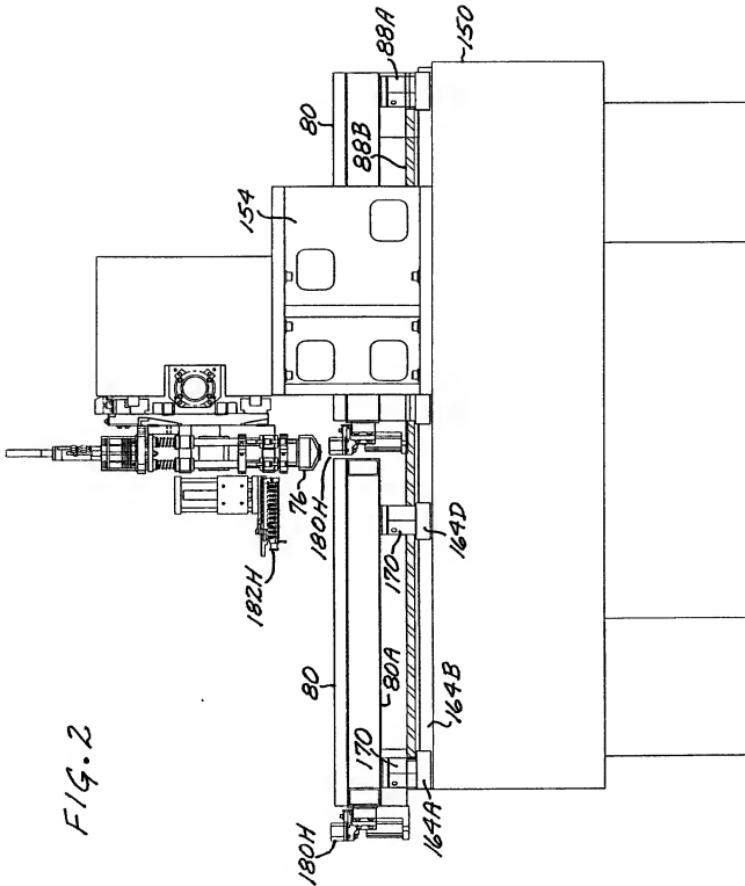


FIG. 2



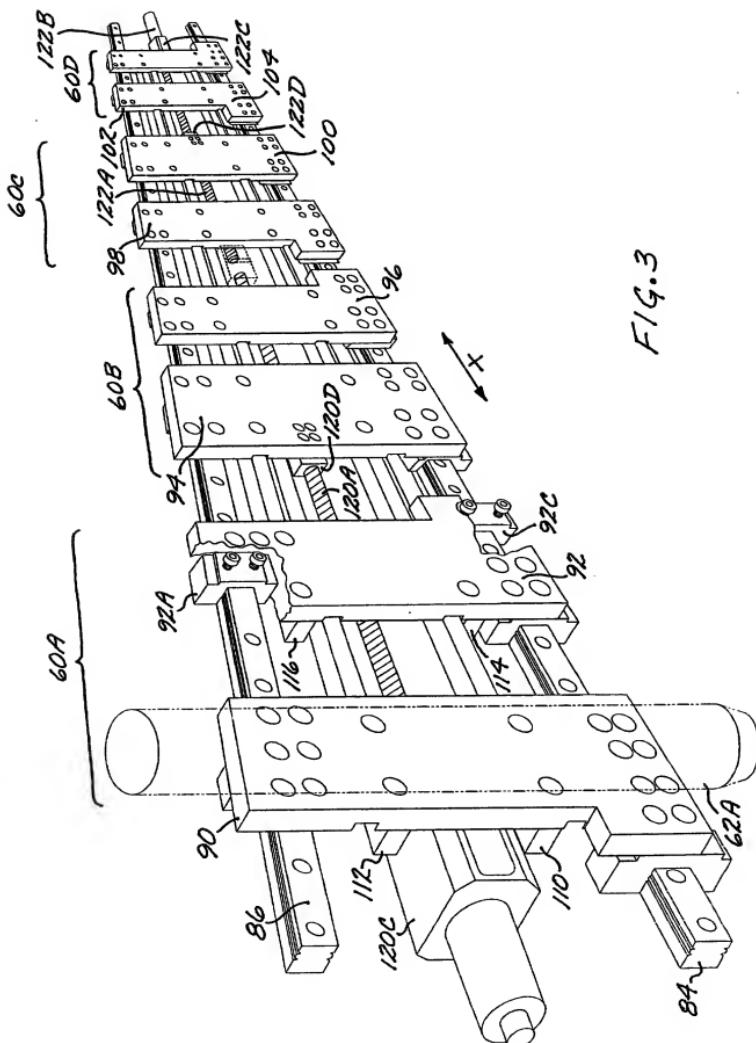
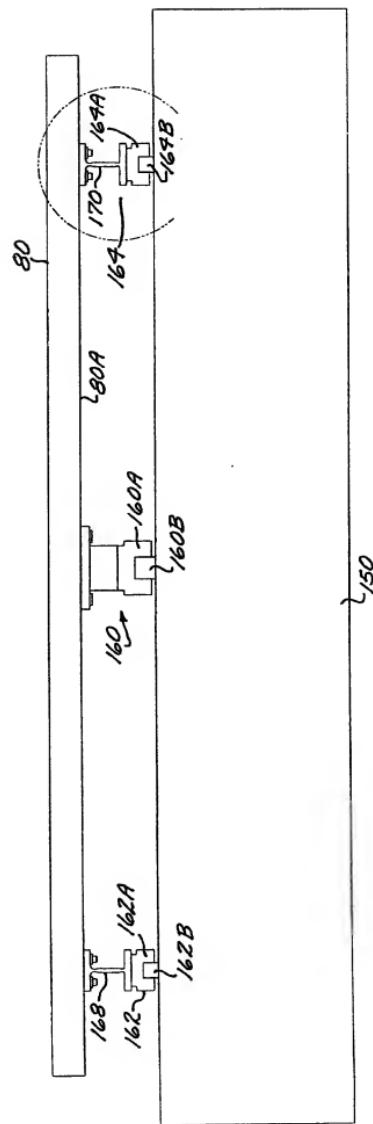


FIG. 4



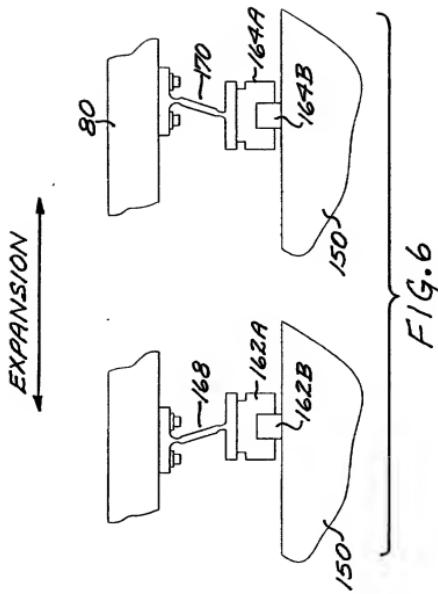


FIG. 5

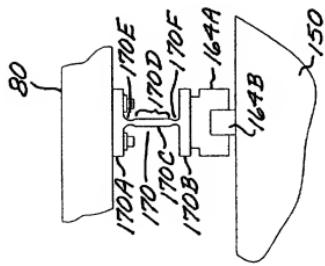


FIG. 6

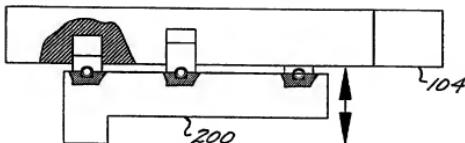


FIG. 9

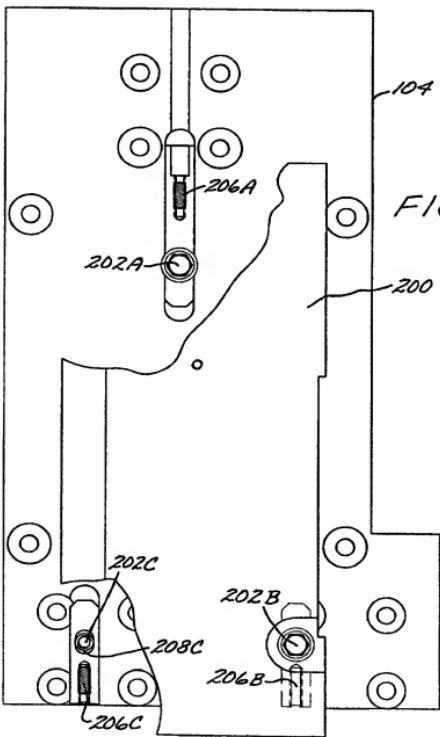
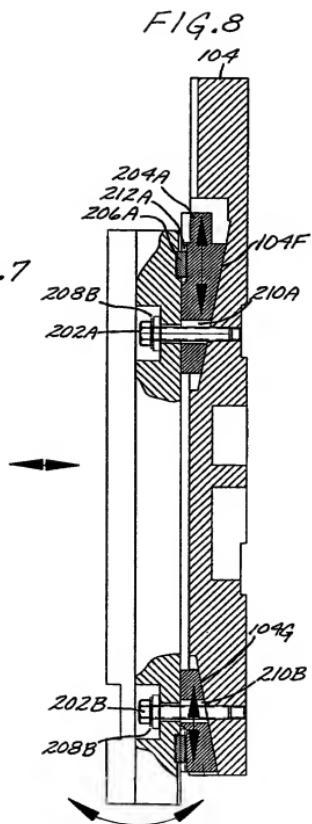
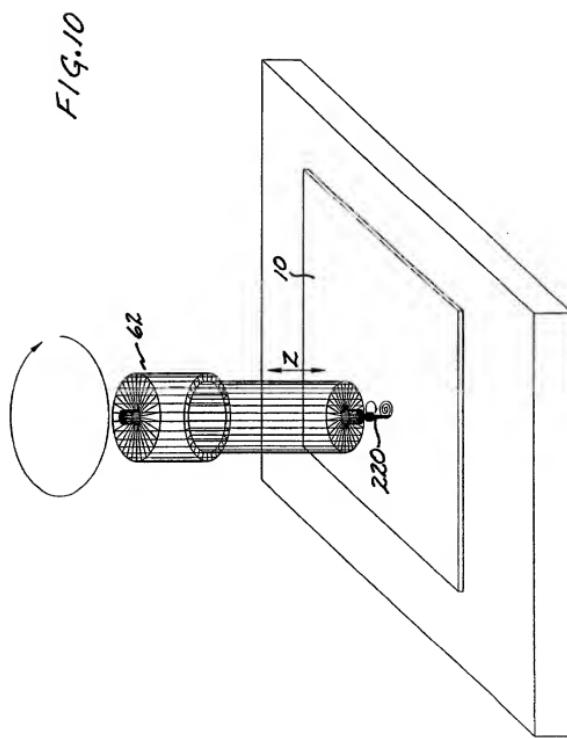
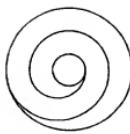


FIG. 7





F/G.10



ROUTER PATH THROUGH THE  
MATERIAL TO EVACUATE HOLES  
VOLUME IN CHIP FORM

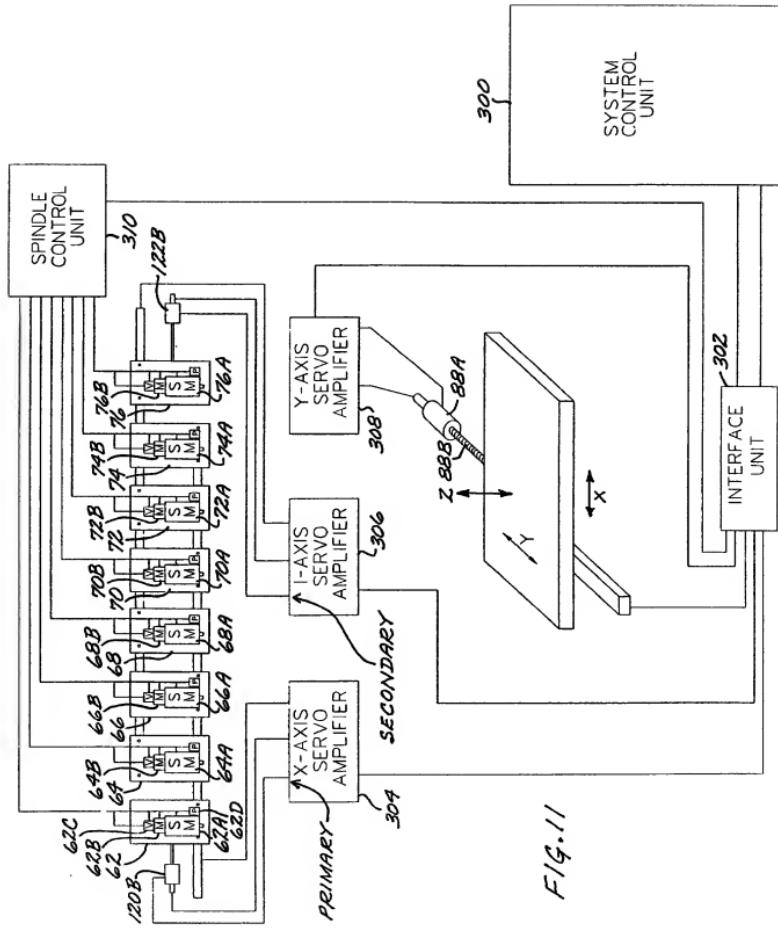


FIG. 12

